

10076237
02/13/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
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| 10076237 | 02/13/2002 | 438 | 480 | 2842 | JEFFREY |

**APPLICANTS: Jang Tae-Ho;

None MC
**CONTINUING DATA VERIFIED:

OK MC
** FOREIGN APPLICATIONS VERIFIED:
REPUBLIC OF KOREA 2001-08111 02/19/2001

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| | | |
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| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed | <input type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO |
| 35 USC 119 conditions met | <input type="checkbox"/> yes <input type="checkbox"/> no | 2522-13 |
| Verified and Acknowledged Examiners's initials <i>KG</i> | | |
| TITLE : Silicon-on-insulator (SOI) substrate and method for manufacturing the same | | |

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
|---|--|----------------------|---------------------|
| | | Total Claims | Print Claim for O.G |
| Assistant Examiner | | DRAWING | |
| | | Sheets Drwg. | Figs. Drwg. |
| Amount Due | | Print Fig. | |
| Date Paid | | | |
| Primary Examiner | | Application Examiner | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | |
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